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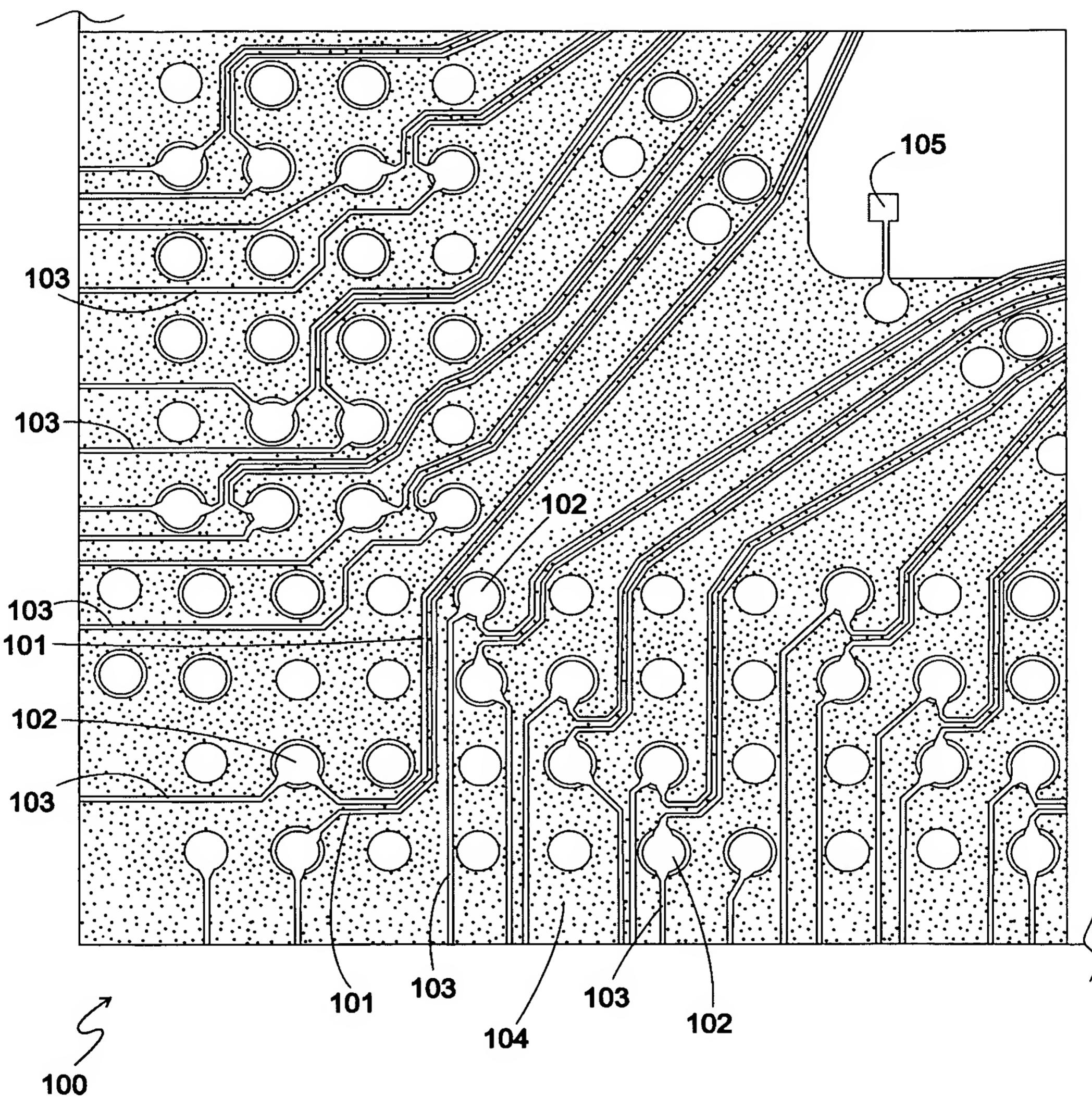


FIG. 1 Prior Art

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Apparatus For Routing Electrical Signals  
Inventor(s): William S Burton

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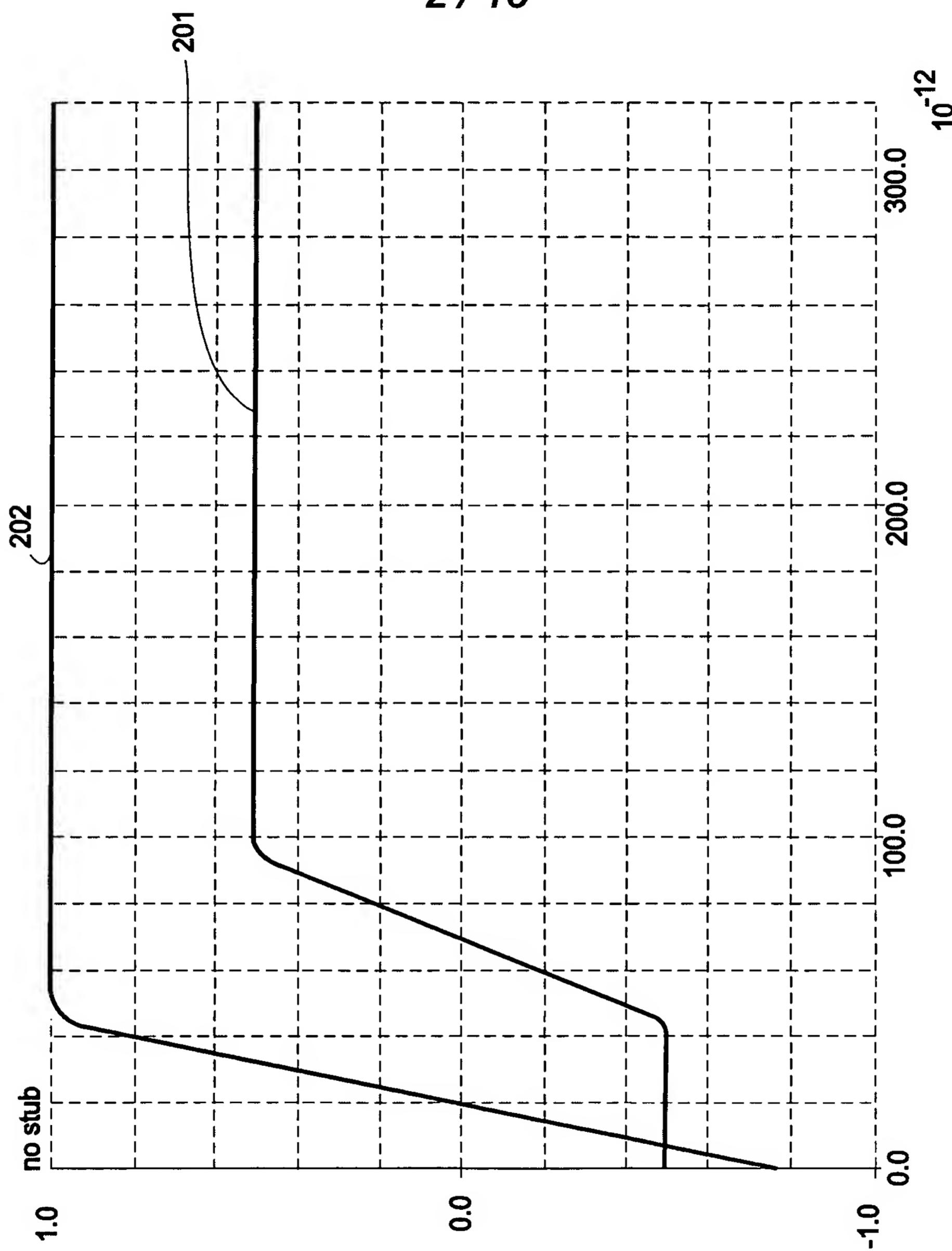


FIG. 2

$\text{Mod}(\text{time} - .32n, 32n)$

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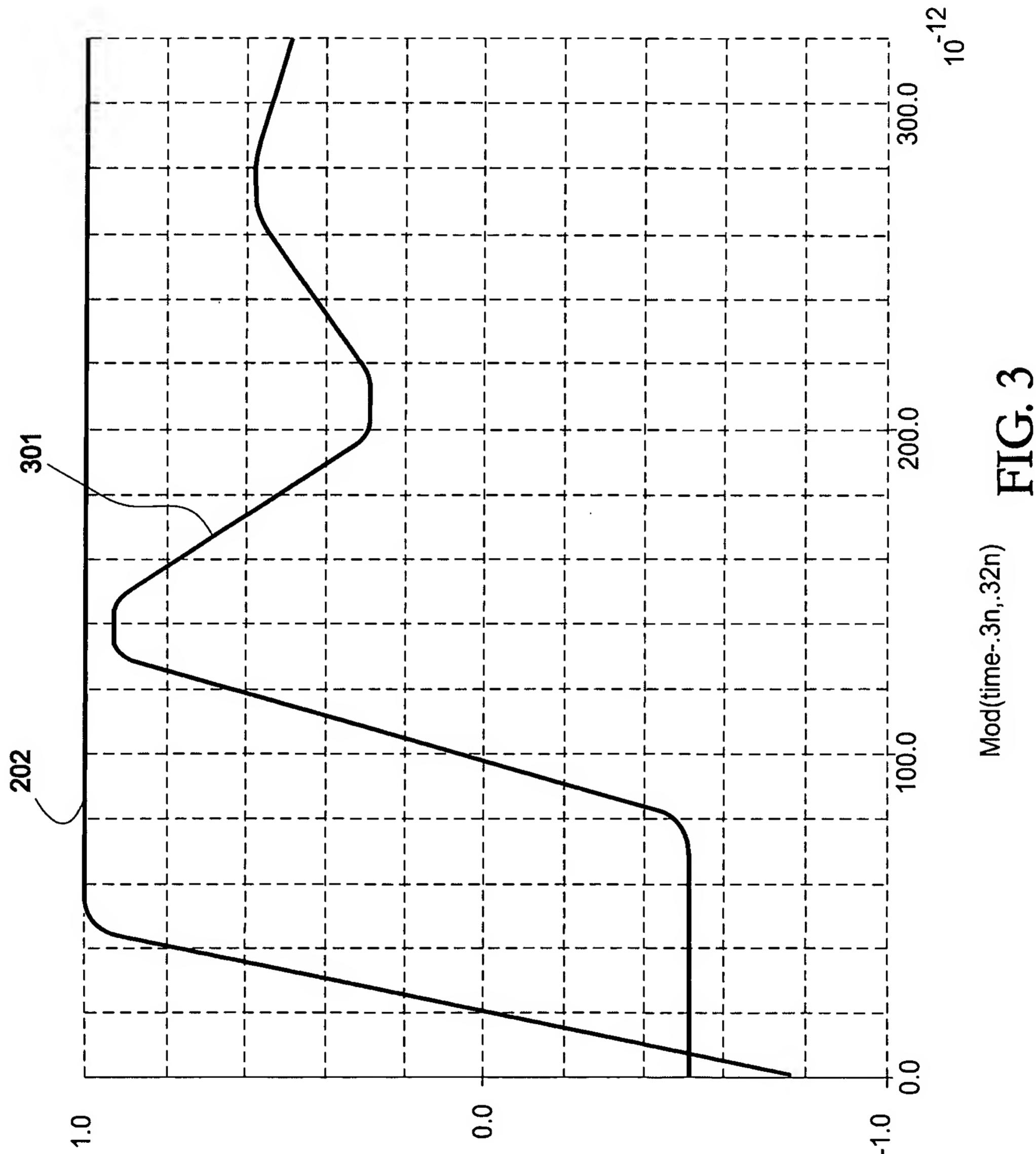
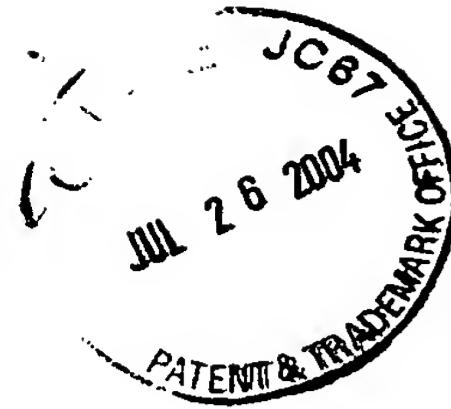


FIG. 3

Mod(time-.3n, 32n)



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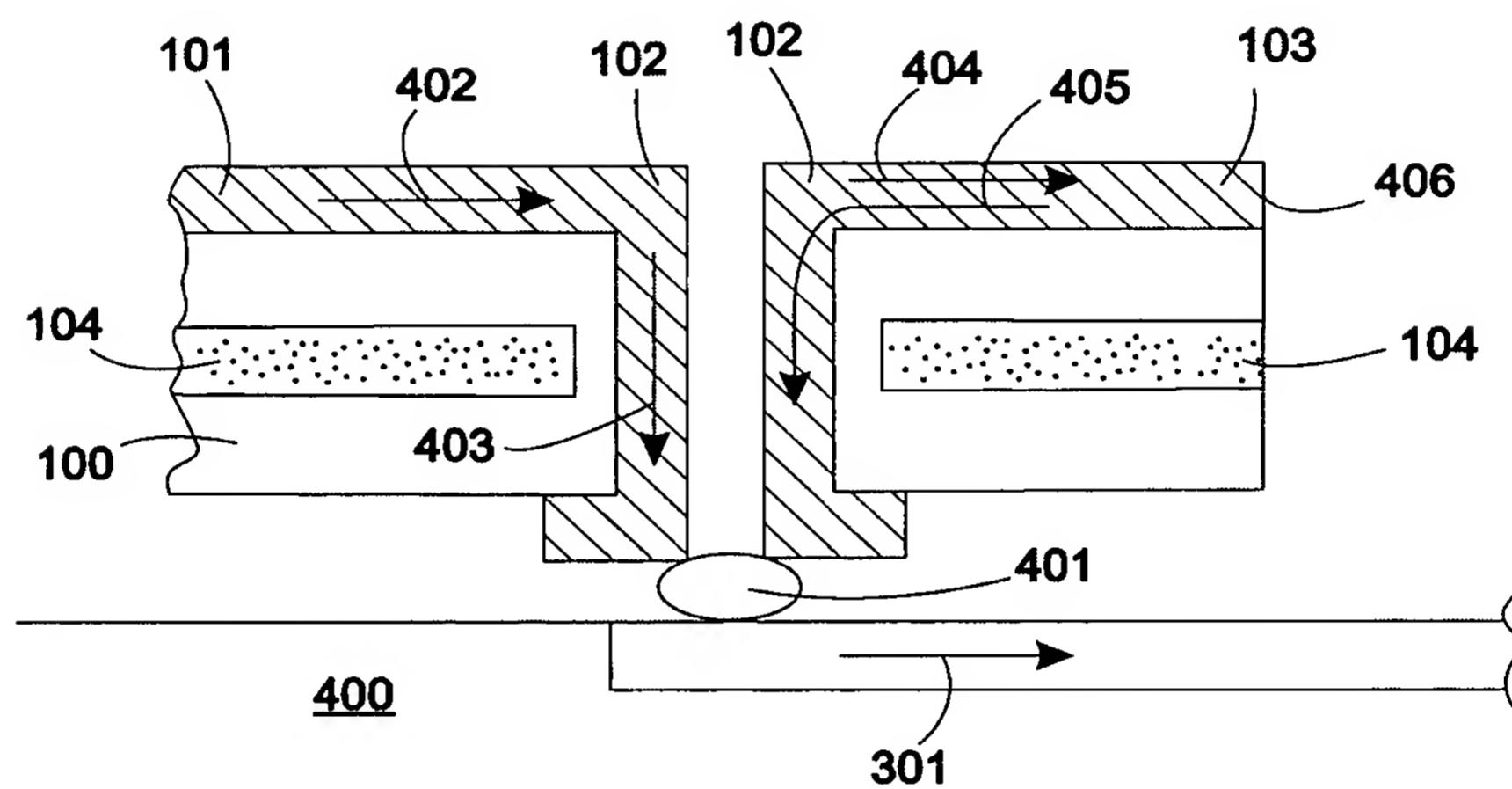
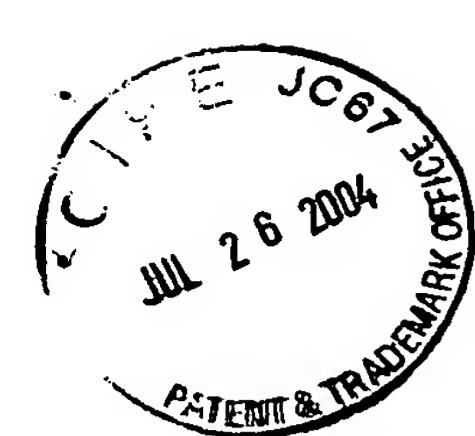


FIG. 4



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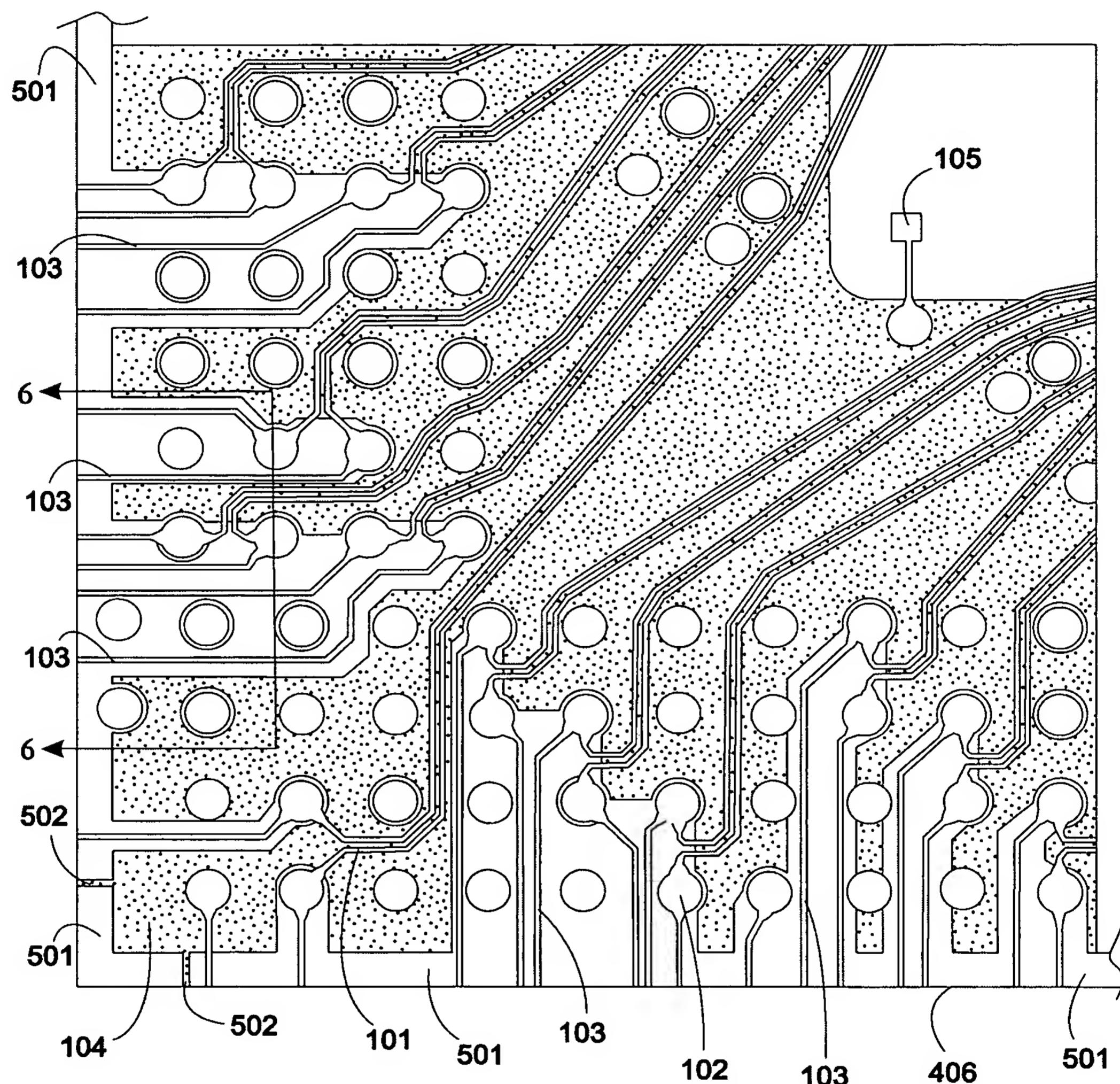
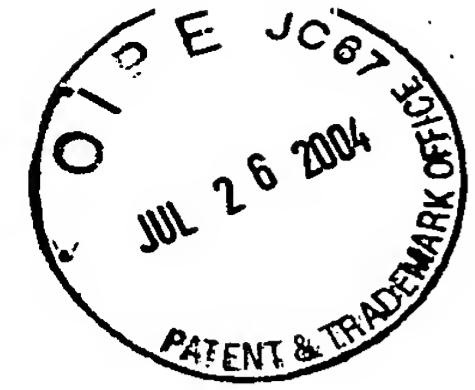


FIG. 5



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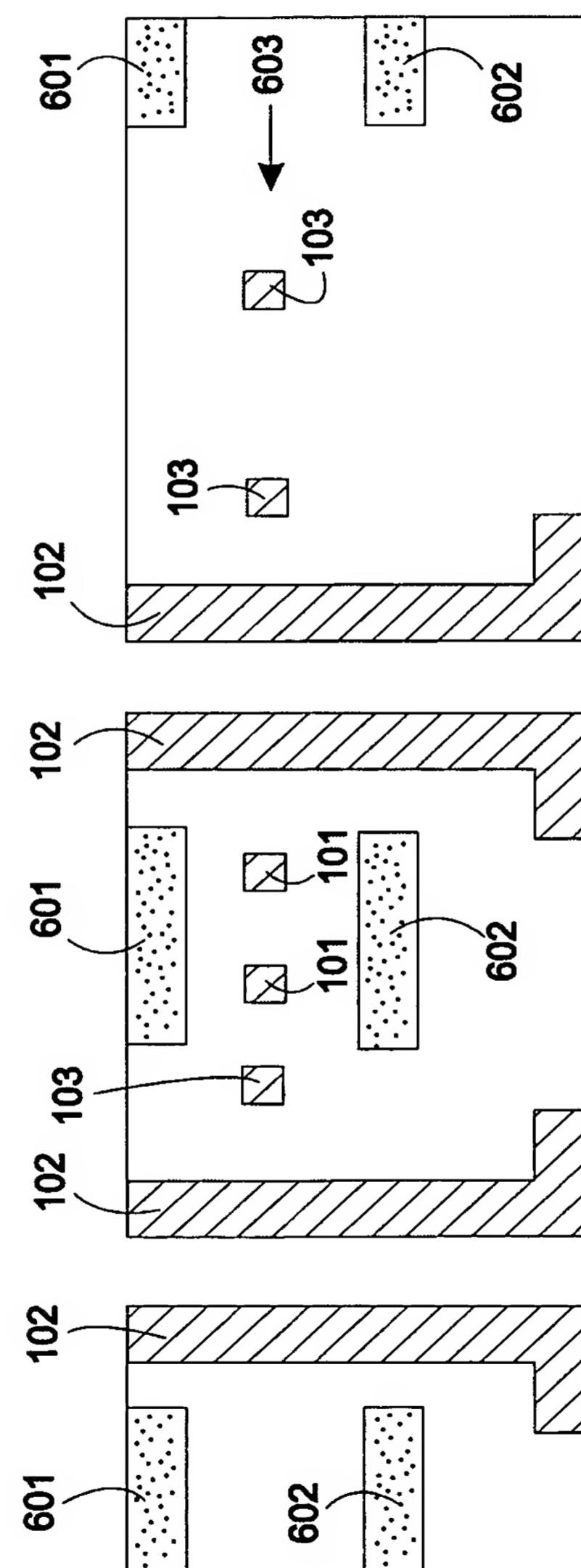
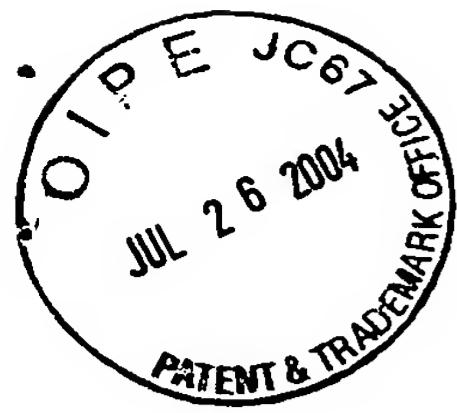


FIG. 6



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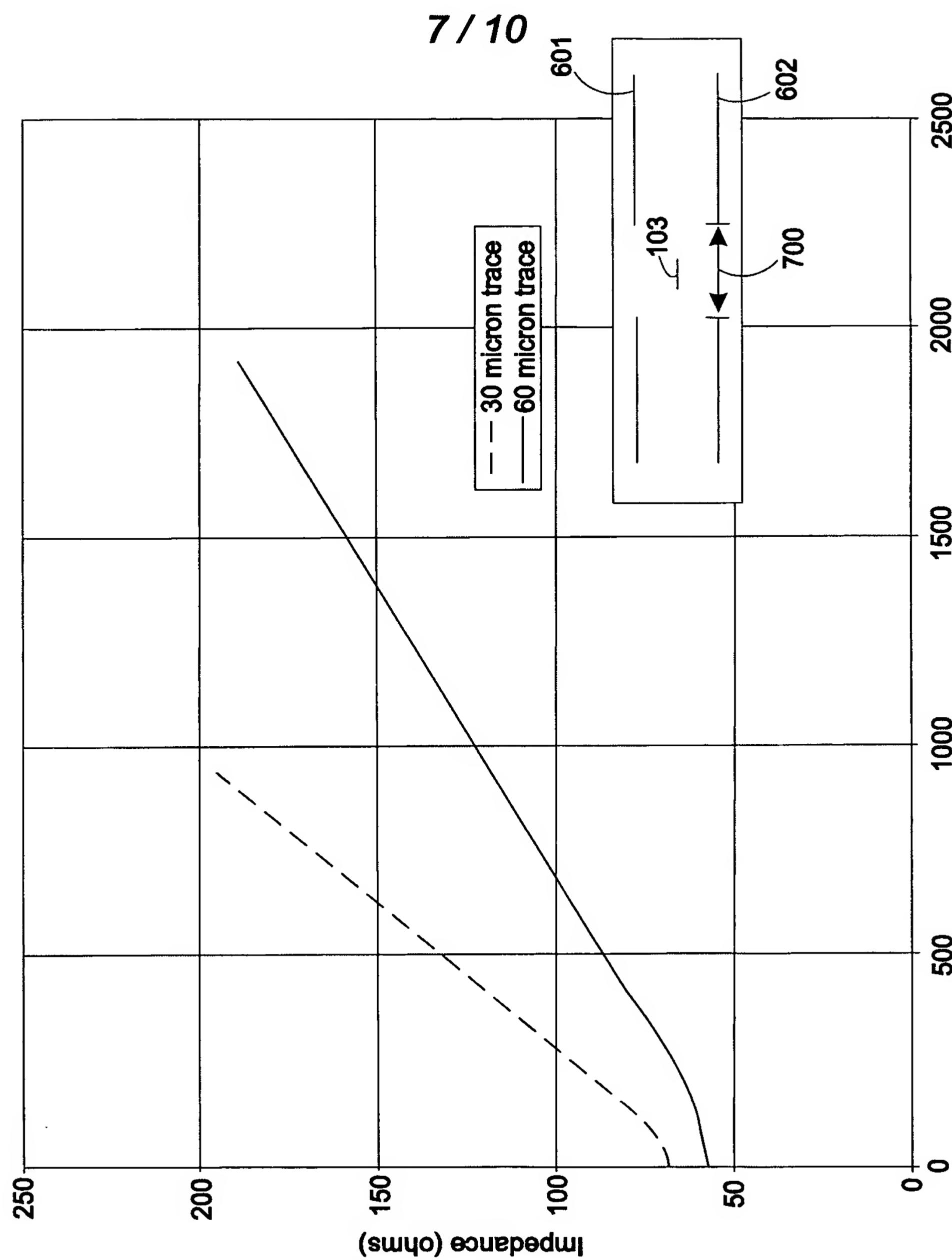


FIG. 7      Gap width above and below trace (microns)



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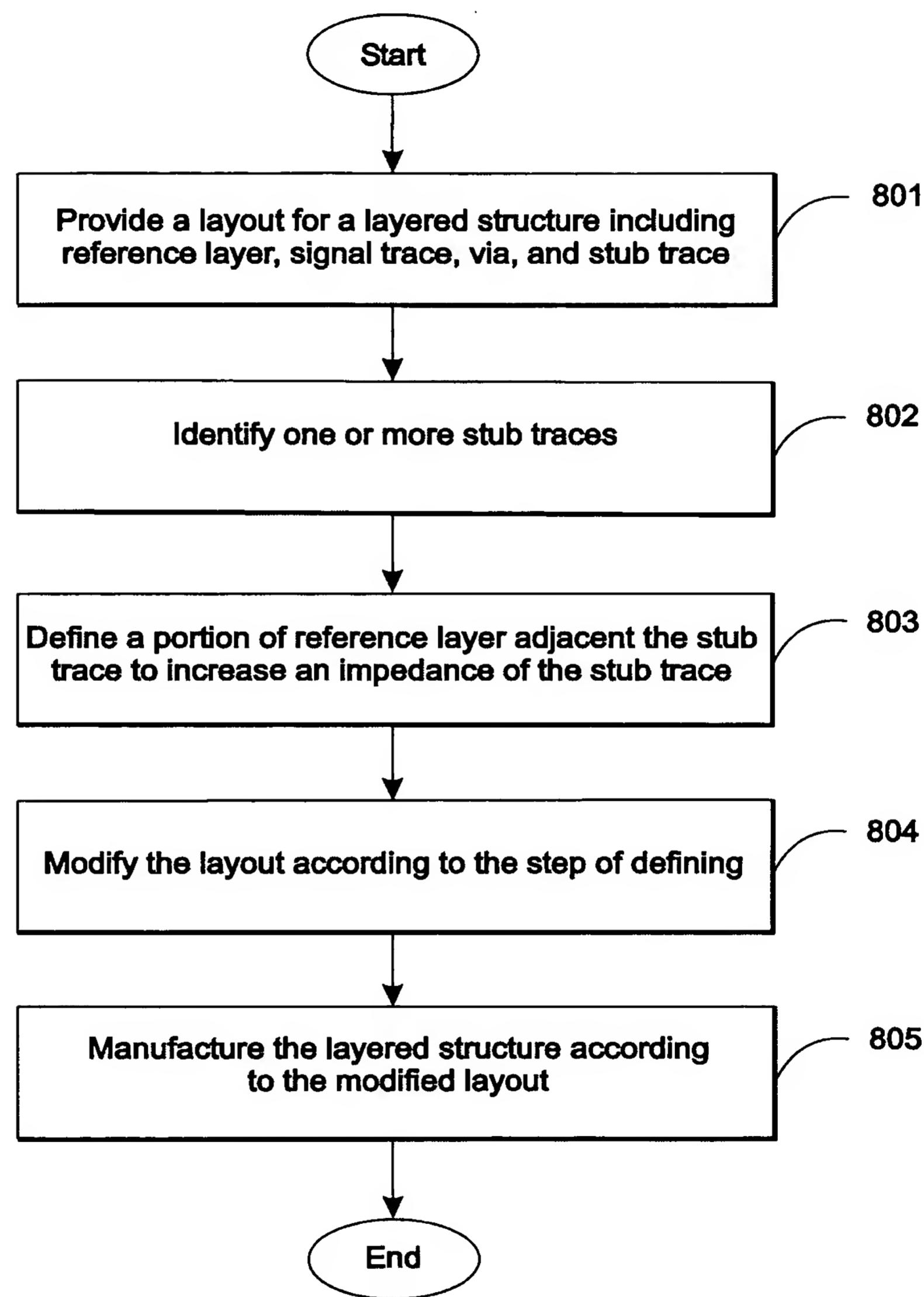
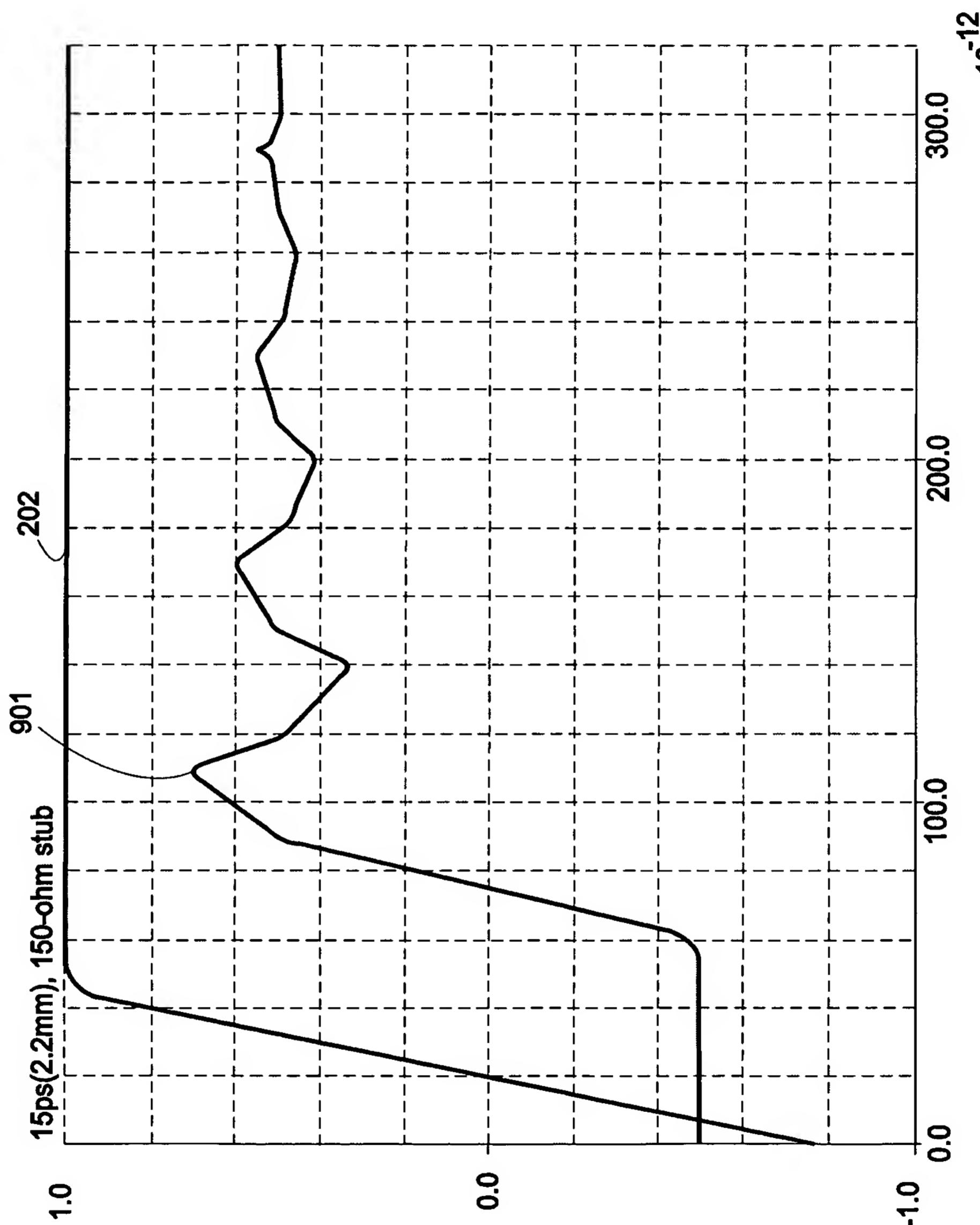


FIG. 8



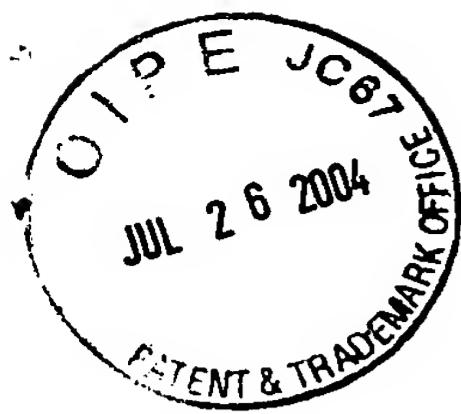
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Mod( $time-3n, 32n$ )

FIG. 9



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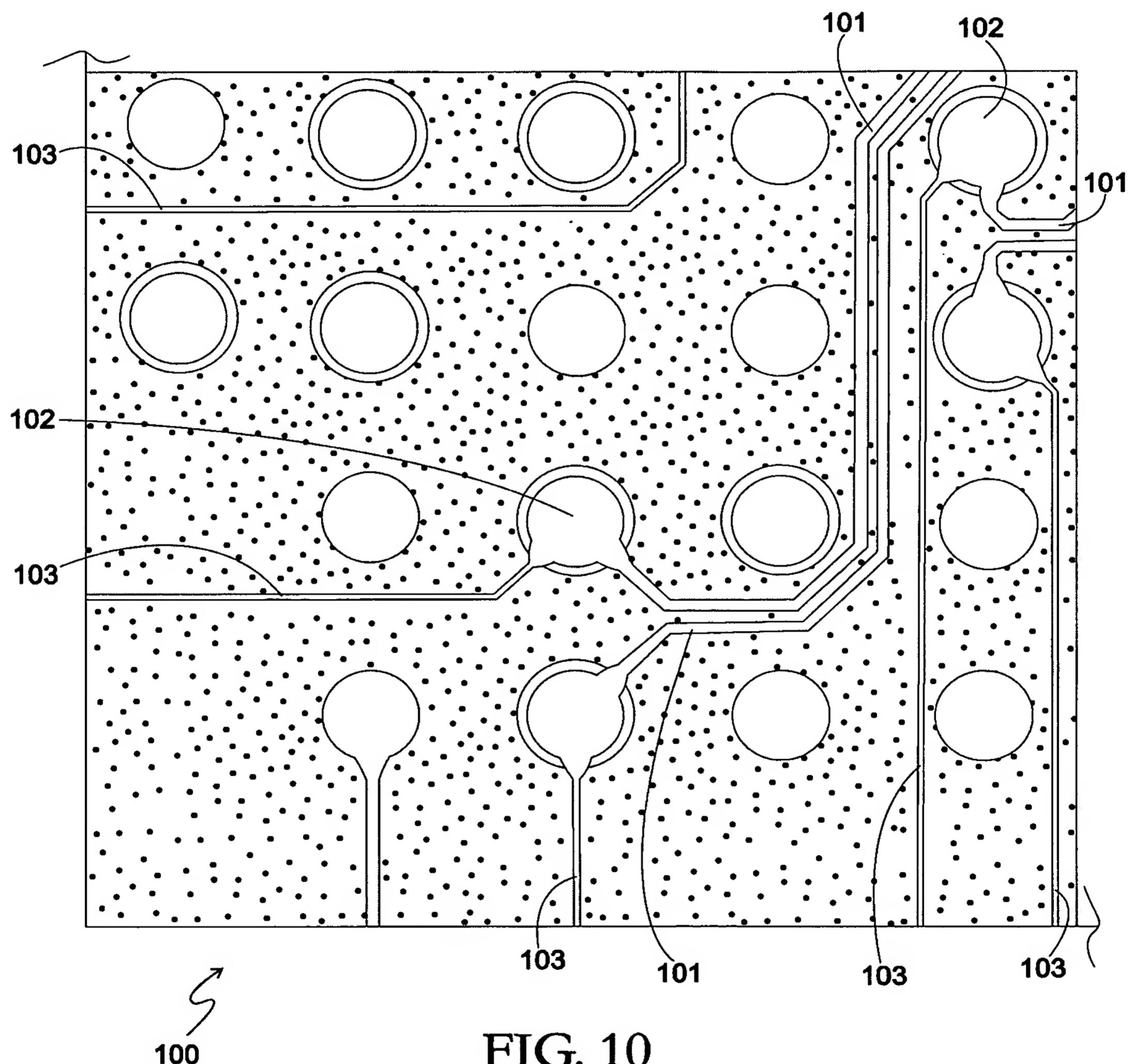


FIG. 10